AMENDMENTS TO THE SPECIFICATION

Please replace the paragraph found at page 12, line 13 with the following:

With such constitution, a refreshing operation that has been heretofore performed on the host side can be placed under the control of the display side, and a processing capability of the host device can be exhibited by performing processing ~~dividually~~ individually on the host side and the display side.

Please replace the paragraph found at page 27, line 2 with the following:

On the other hand, a plurality of panel control chips 51, each having a post-processor 70 therein, are provided on the panel side 50. In Fig. 1, in accordance with the division of the panel 55 into four areas, the divided panel 55 is driven by use of the four panel control chips 51. Reference numeral 52 denotes a panel memory provided in each panel control chip 51. Reference numeral 54 denotes a panel data outputted for transmitting the output from the panel control chip 51 to the panel 55. Moreover, reference numeral 55 denotes a panel for actually displaying an image, and the panel 55 is divided into the four areas in Fig. 1. This panel 55 is constituted by a high-resolution panel, and to support this high-resolution screen, the panel control chip 51 makes it possible to perform plural parallel ~~processings~~ processing. Moreover, reference numeral 68 denotes an external data bus, and reference numeral 69 denotes an intra-panel data bus.

Please replace the paragraph found at page 41, line 14 with the following:

Fig. 11 is a block diagram for explaining a system constitution of the multi-system in this embodiment. As shown in Fig. 11, the panel 55 is divided into four parts by the display dividing

lines 56, and the four parts are connected to the respective panel control chips 51. However, there is not always necessary to provide a plurality of panel control chips 51, but it is possible to constitute the multi-system so that the panel 55 is controlled by one control panel. To each of the respective panel control chips 51, video data is sent from the corresponding graphics chip 11 in different systems 100. On the panel side 50, the panel control chips 51 are connected to each other by the intra-panel data ~~bust~~ bus 69 so that one system 100 can simultaneously control the whole of the screen.